

OpenCL Matrix Multiplication



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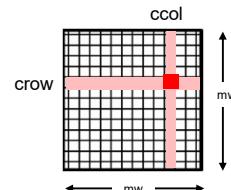
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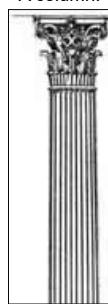
openclMatrixMult.pptx

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Matrices

A matrix is a 2D array of numbers, arranged in rows that go across and columns that go down:

A column:



4 columns →

3 rows ↓

$$\begin{bmatrix} 1 & 2 & 3 & 4 \\ 5 & 6 & 7 & 8 \\ 9 & 10 & 11 & 12 \end{bmatrix}$$



Matrix sizes are termed “#rows x #columns”, so this is a 3x4 matrix

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Square Matrices

A square matrix has the same number of rows and columns

The diagram shows a 3x3 matrix with three rows and three columns. A green arrow labeled "3 rows" points downwards from the top row to the bottom row. A green arrow labeled "3 columns" points to the right from the leftmost column to the rightmost column. The matrix is defined by the following brackets:

$$\begin{bmatrix} 1 & 2 & 3 \\ 4 & 5 & 6 \\ 7 & 8 & 9 \end{bmatrix}$$

This is a 3x3 matrix



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Matrix Multiplication

The basic operation of matrix multiplication is to pair-wise multiply a single row by a single column

The diagram illustrates the multiplication of two matrices, A and B, to produce matrix C. Matrix A is a 1x3 matrix with elements 1, 2, and 3. Matrix B is a 3x1 column vector with elements 4, 5, and 6. The resulting matrix C is a 1x1 scalar value of 32. The calculation is shown as $4*1 + 5*2 + 6*3 \rightarrow 32$. Dotted orange ovals group the elements of matrix A into pairs with the corresponding elements of matrix B: (1, 4), (2, 5), and (3, 6). Braces indicate the row and column indices: a brace over the first two columns of A indicates it is a 1x3 row, and a brace to the right of B indicates it is a 3x1 column.



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Matrix Multiplication

Two matrices, A and B, can be multiplied if the number of columns in A equals the number of rows in B. The result is a matrix that has the same number of rows as A and the same number of columns as B.

$$\begin{bmatrix} [1 & 2 & 3] \\ \mathbf{A} \end{bmatrix} * \begin{bmatrix} 4 \\ 5 \\ 6 \end{bmatrix} \Rightarrow \begin{bmatrix} 32 \end{bmatrix} \quad \mathbf{C}$$



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Matrix Multiplication in Software

Here's how to remember how to do it:

1. $\mathbf{C} = \mathbf{A} * \mathbf{B}$

$$\begin{bmatrix} [1 & 2 & 3] \\ \mathbf{A} \end{bmatrix} * \begin{bmatrix} 4 \\ 5 \\ 6 \end{bmatrix} \Rightarrow \begin{bmatrix} 32 \end{bmatrix} \quad \mathbf{C}$$

2. $[I \times J] = [I \times K] * [K \times J]$

$$\text{---} \circled{I \times J} \text{---} = \text{---} \circled{I \times K} \text{---} \circled{K \times J} \text{---}$$

C[i][j] += A[i][k] * B[k][j] ;



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Matrix Multiplication in CPU Software

$$\begin{bmatrix} [1 & 2 & 3] \\ \mathbf{A} \end{bmatrix} * \begin{bmatrix} \{4\} \\ \{5\} \\ \{6\} \end{bmatrix} \Rightarrow \begin{bmatrix} 32 \\ \mathbf{C} \end{bmatrix}$$

```

for( int i = 0; i < numRows; i++ )
{
    for( int j = 0; j < numBcols; j++ )
    {
        C[ i ][ j ] = 0.;

        for( int k = 0; k < numAcols; k++ )
        {
            C[ i ][ j ] += A[ i ][ k ] * B[ k ][ j ];
        }
    }
}

```



Note: numAcols *must == numBrows !*

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Matrix Multiplication in CPU Software

$$\begin{bmatrix} [1 & 2 & 3] \\ \mathbf{A} \end{bmatrix} * \begin{bmatrix} \{4\} \\ \{5\} \\ \{6\} \end{bmatrix} \Rightarrow \begin{bmatrix} 32 \\ \mathbf{C} \end{bmatrix}$$

Note that saying:

```

C[ i ][ j ] = 0.;

for( int k = 0; k < numAcols; k++ )
{
    C[ i ][ j ] += A[ i ][ k ] * B[ k ][ j ];
}

```

Is like saying:

$C[i][j] = A[i][0] * B[0][j] + A[i][1] * B[1][j] + A[i][2] * B[2][j] + A[i][3] * B[3][j] \dots$



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Doing it in OpenCL: #defines, #includes, and Globals

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```
#include <stdio.h>
#include <math.h>
#include <string.h>
#include <stdlib.h>
#include <omp.h>
#include "cl.h"
#include "cl_platform.h"

// the matrix-width and the number of work-items per work-group:
// note: the matrices are actually MATWxMATW and the work group sizes are LOCALSIZExLOCALSIZE:
#ifndef MATW
#define MATW      1024
#endif

#ifndef LOCALSIZE
#define LOCALSIZE   8
#endif

// opencl objects:
cl_platform_id    Platform;
cl_device_id      Device;
cl_kernel          Kernel;
cl_program         Program;
cl_context         Context;
cl_command_queue   CmdQueue;

float             hA[MATW][MATW];
float             hB[MATW][MATW];
float             hC[MATW][MATW];

const char *      CL_FILE_NAME = { "proj06.cl" };


```

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The .cl Kernel Function

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```
#define IN
#define OUT

kernel
void
MatrixMult( IN global const float *dA, IN global const float *dB, IN global int *dMW, OUT global float *dC )
{
    // [dA] is dMW x dMW
    // [dB] is dMW x dMW
    // [dC] is dMW x dMW
    // but all the matrix's rows are really linear in memory
    int mw = *dMW;
    int crow = get_global_id( 0 ); // which row we are filling
    int ccol = get_global_id( 1 ); // which column we are filling

    int aindex = crow * mw; // a[i][0]
    int bindex = ccol; // b[0][j]
    int cindex = crow * mw + ccol; // c[i][j]

    float cij = 0.;
    for( int k = 0; k < mw; k++ )
    {
        cij += dA[aindex] * dB[bindex];
        aindex++;
        bindex += mw;
    }
    dC[cindex] = cij;
}
```

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Setting Up the Memory for the Matrices

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```
int mw = MATW;
size_t aSize = MATW * MATW * sizeof(float);
size_t bSize = MATW * MATW * sizeof(float);
size_t mwSize = sizeof(mw);
size_t cSize = MATW * MATW * sizeof(float);

cl_mem dA = clCreateBuffer( Context, CL_MEM_READ_ONLY, aSize, NULL, &status );
...
status = clEnqueueWriteBuffer( CmdQueue, dA, CL_FALSE, 0, aSize, hA, 0, NULL, NULL );
...
Wait( CmdQueue );
```



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Setting up the Kernel Function Arguments

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Remember that our kernel function looks like this:

```
kernel
void
MatrixMult( IN global const float *dA, IN global const float *dB, IN global int *dMW, OUT global float *dC )
```

So the definition of the arguments needs to look like this:

```
Kernel = clCreateKernel( Program, "MatrixMult", &status );

status = clSetKernelArg( Kernel, 0, sizeof(cl_mem), &dA );
status = clSetKernelArg( Kernel, 1, sizeof(cl_mem), &dB );
status = clSetKernelArg( Kernel, 2, sizeof(cl_mem), &dMW );
status = clSetKernelArg( Kernel, 3, sizeof(cl_mem), &dC );
```



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```
size_t globalWorkSize[3] = { MATW,      MATW,      1 };
size_t localWorkSize[3] = { LOCALSIZE, LOCALSIZE, 1 };

Wait( CmdQueue );

double time0 = omp_get_wtime();

status = clEnqueueNDRangeKernel( CmdQueue, Kernel, 1, NULL,
                                globalWorkSize, localWorkSize, 0, NULL, NULL );

Wait( CmdQueue );
double time1 = omp_get_wtime();
```



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```
// performance in giga-multiplies performed per second:
printf( stderr, "GigaMultsPerSecond: %10.2f\n",
        (double)MATW*(double)MATW*(double)MATW/(time1-time0)/1000000000. );
```



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Copying the Resulting Matrix from the Device back to the Host

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```
status = clEnqueueReadBuffer( CmdQueue, dC, CL_FALSE, 0, cSize, hC, 0, NULL, NULL );  
Wait( CmdQueue );
```



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Cleaning Up

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```
clReleaseKernel( Kernel );  
clReleaseProgram( Program );  
clReleaseCommandQueue( CmdQueue );  
  
clReleaseMemObject( dA );  
clReleaseMemObject( dB );  
clReleaseMemObject( dMW );  
clReleaseMemObject( dC );
```



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